

Shared Memory Interface to UT69151 SμMMIT™

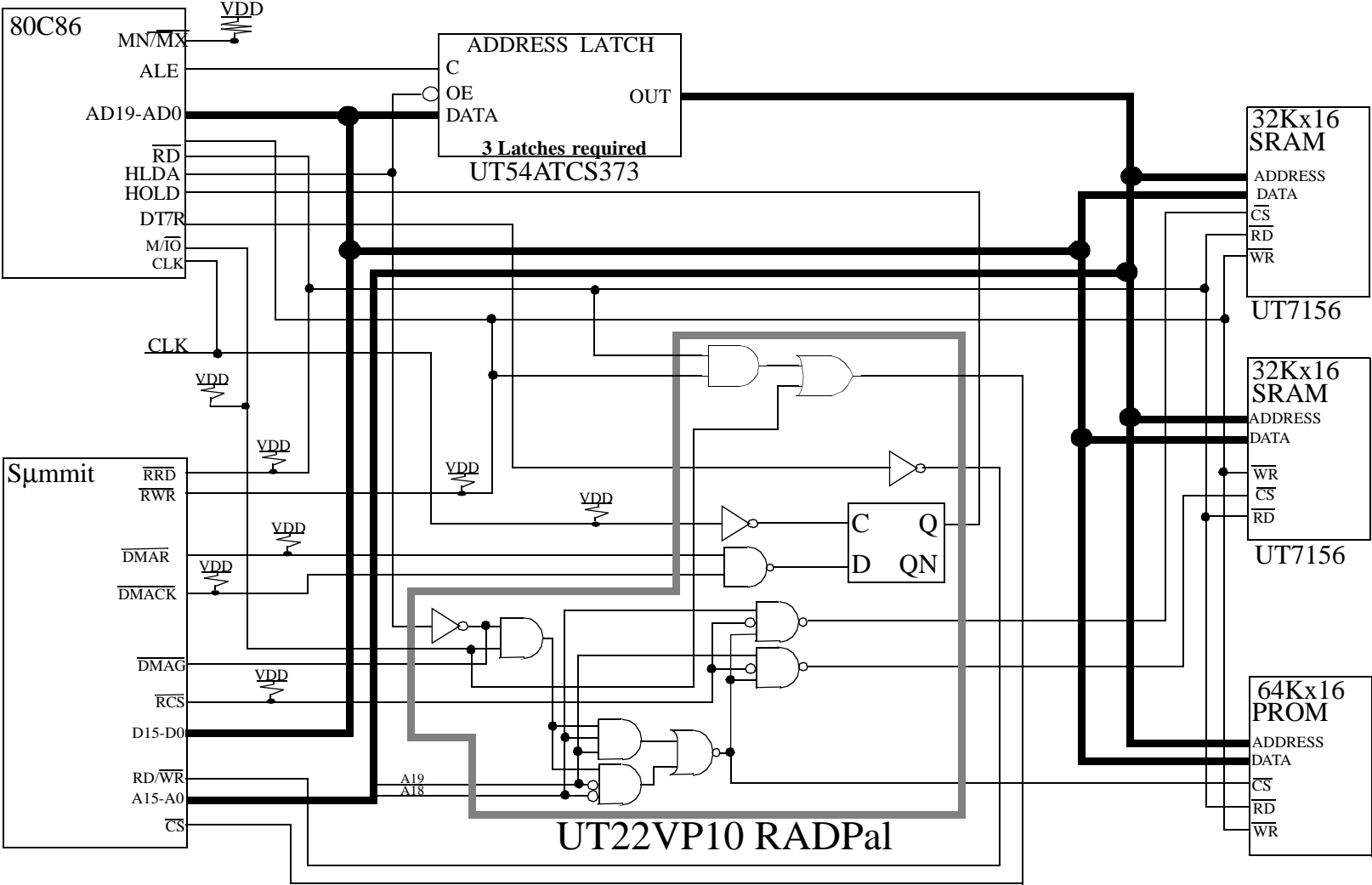


FIGURE 1. SμMMIT TO 80C86 CONNECTION DIAGRAM

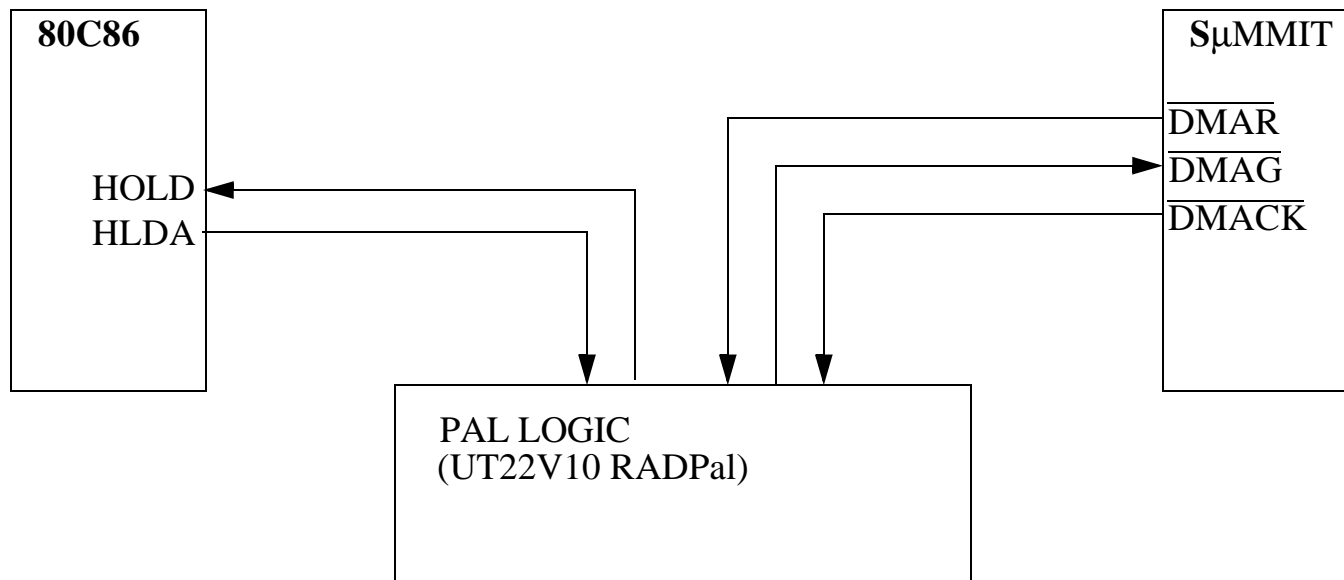
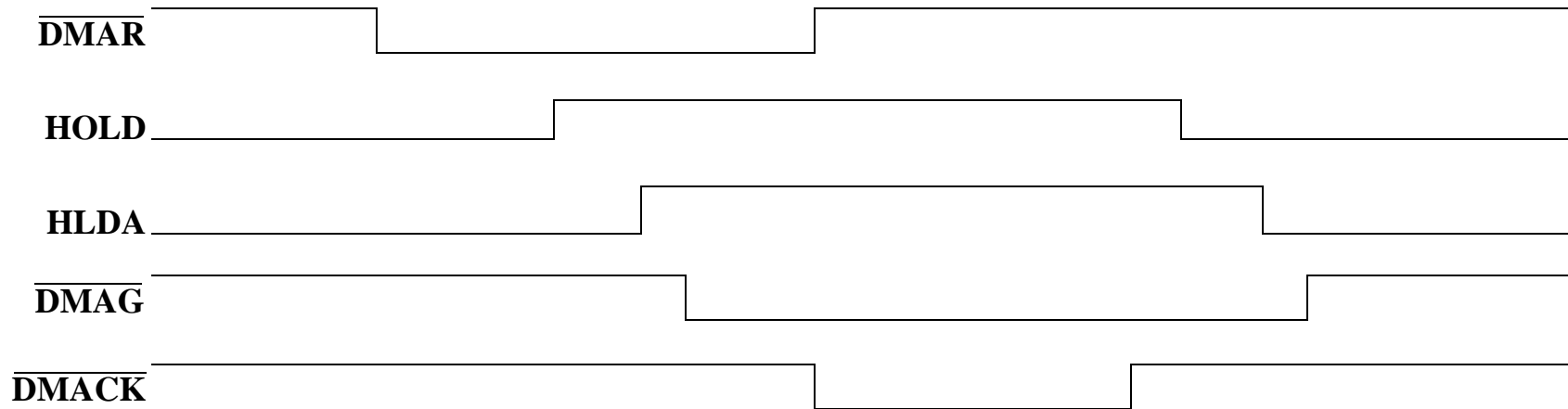


FIGURE 2. BASIC BUS ARBITRATION DIAGRAM

Industry 80C86: Read Cycle

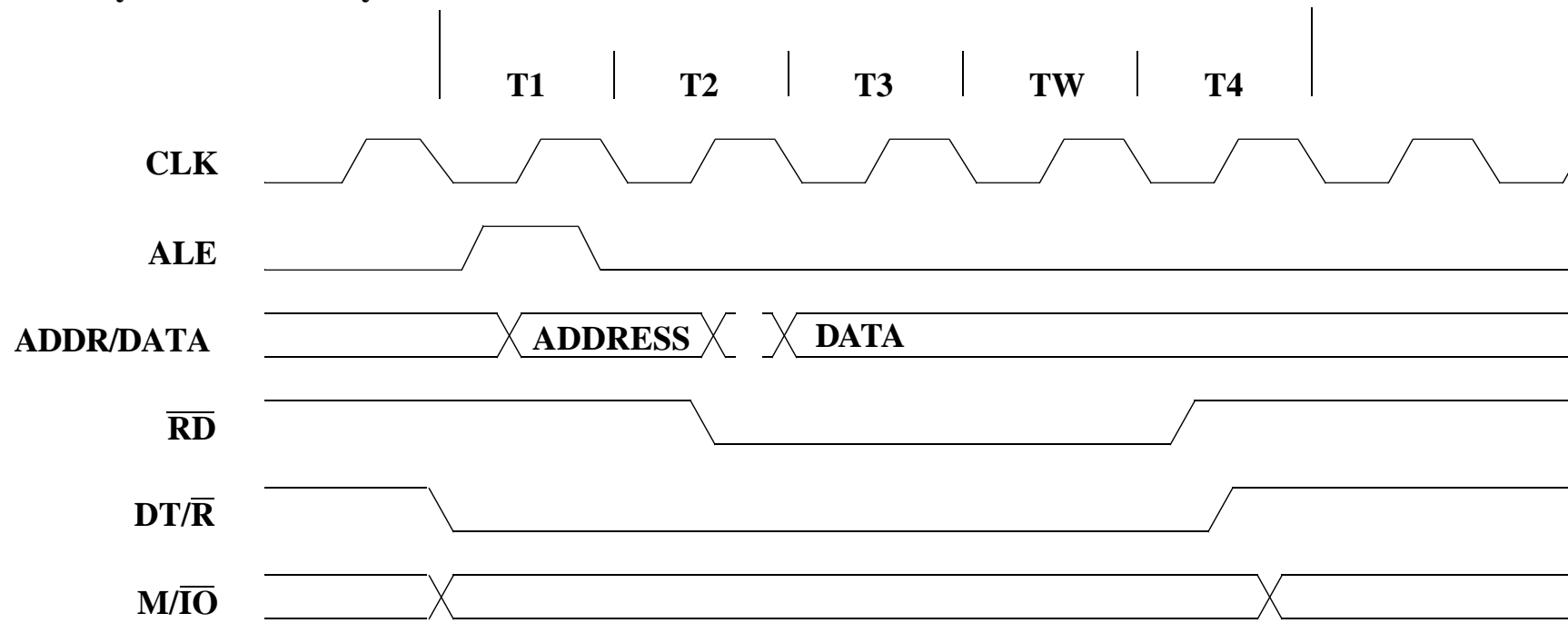


FIGURE 3. BASIC READ TIMING DIAGRAM

Industry 80C86: Write Cycle

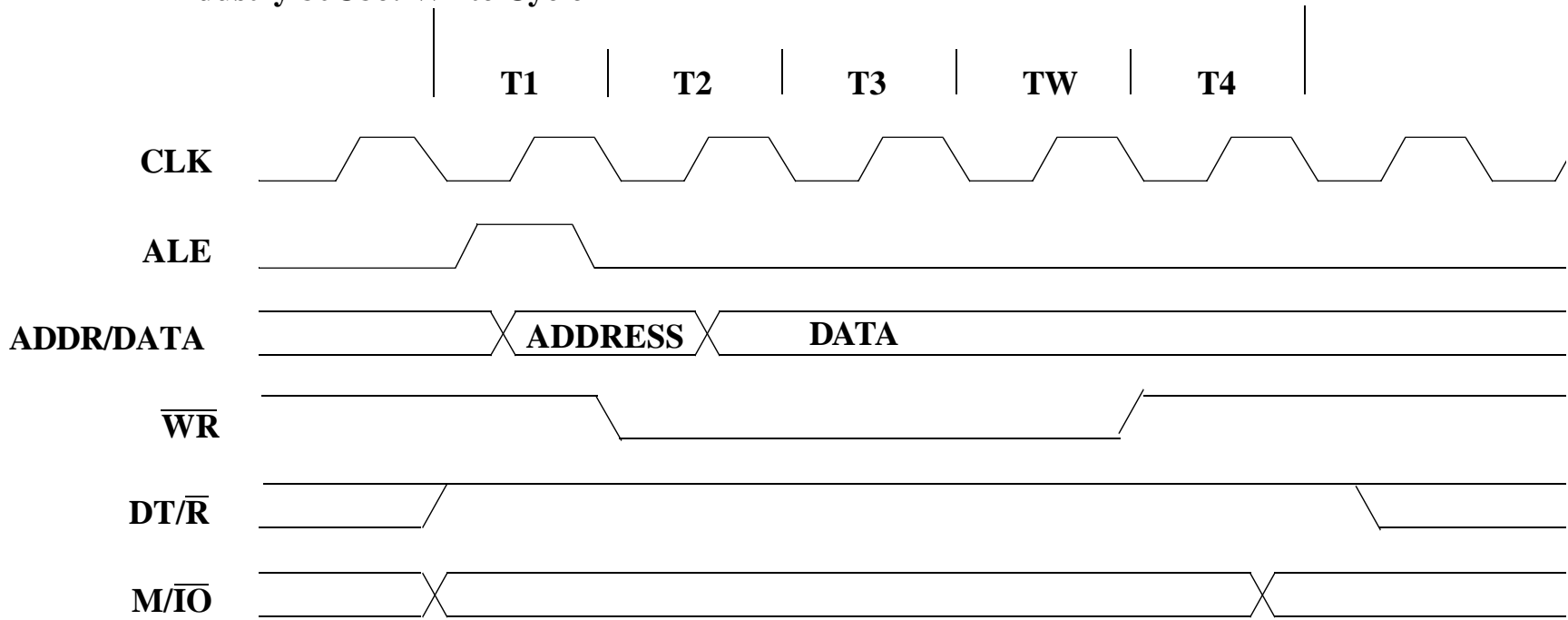


FIGURE 4. BASIC WRITE TIMING DIAGRAM

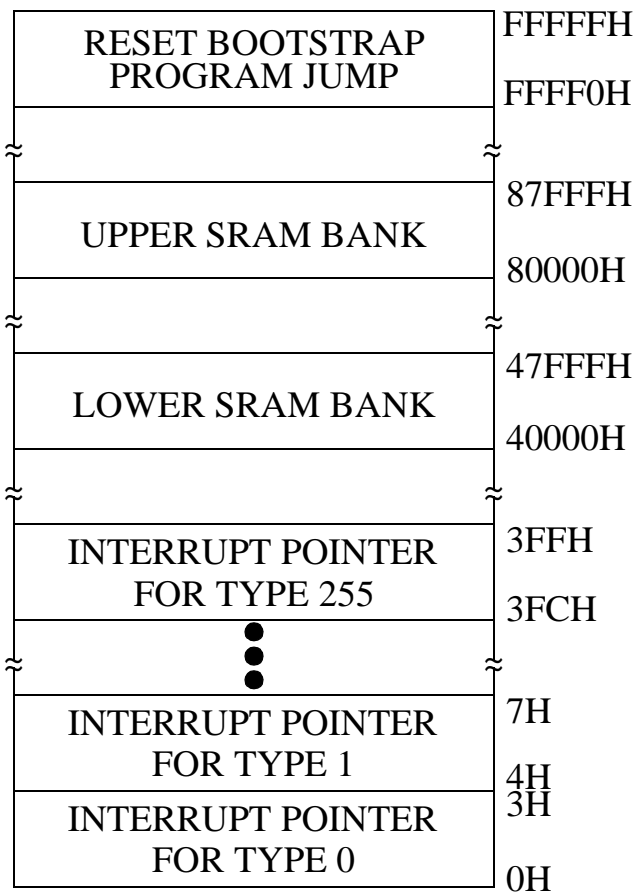


FIGURE 5. MEMORY LOCATIONS